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WI		High Performance Stacked LDD RF LDMOSFET by Jun Cai et al.					
Will		120 V Interdigitated-Drain LDMOS (IDLDMOS) on SOI Substrate Breaking Power LDMOS Limit by Shuming Xu et al.					
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U.S. PATENT DOCUMENTS							
Examiner Initials	Cite No.1	Document Number  Number - Kind Code <sup>2</sup> (if known)	Publication Date or Issue Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
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Based on PTO/SB/08A (05-03) as modified by Blakely, Solokoff, Taylor & Zalman (wir) 05/02/2003.

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